



**METHOD AND MEANS FOR INCREASING INHERENT CHANNEL
CAPACITY FOR WIRED NETWORK**

CROSS REFERENCE TO RELATED APPLICATIONS

[01] This application is filed under 35 U.S.C. § 111(a), claiming benefit pursuant to 35 U.S.C. § 119(e)(1) of the filing date of the Provisional Application 60/154,781 filed on September 20, 1999 pursuant to 35 U.S.C. § 111(b). The Provisional Application is incorporated herein by reference for all it discloses.

BACKGROUND OF INVENTION

[02] This invention relates to a unique receiver system which improves the Signal-to-Noise ratio capability of receiving signals, compared to conventional receiver practices, and by processing stored received data in near-real time, reduces the inherent affect of thermal noise. This dramatic improvement in the performance affords greater flexibility with respect to several relevant parameters, such as for example, bandwidth, access time, and multiplex ability and accordingly is beneficial in a wide range of commercial and military markets. The invention is applicable to a variety of wireline telecommunications media and data applications including the "Internet".

[03] There are few communication systems that cannot benefit from a significant reduction in the inherent thermal noise that results from necessary amplification. Previous efforts have been expended to conceive, improve and develop a "wireless" antenna and receiving system that has a dramatically improved signal-to-noise ratio and related characteristics. In this process extensive software programs were conceived and developed for both simulation and solution-to-problem purposes. These techniques have been adapted to wired communication. The result includes means for greatly improving the signal-to-noise ratio that can be obtained

from a sequence of digitally converted received signals that are stored and then processed in a wired system suited to the “INTERNET”.

[04] The description herein describes the signal-to-noise improvements and involves both “hardware” and “software”. These unique features provide the potential for increasing channel capacity and other performance improvements. These opportunities for improvement occur at several links in the operation such as with the service provider(s) and at various “gateways” that receive the communicated modulated signal.

[05] Processing is achieved using modern integrated circuits in an off-line manner that does not adversely compromise the bandwidth of the system. The time for such processing results in a “transport” time delay, which can be made tolerable. The resulting “near real time” performance provides the potential to obtain dramatic S/N improvements beyond that predicted by classic analog developed theory.

[06] Thermal noise is introduced as a result of necessary amplification in the reception of a signal; such noises are usually a limiting factor in the ability to identify a “weak” signal. If such noise is substantially reduced, there results a potential for improving the receptivity of the signal, thereby also allowing for tradeoffs in parameters such as bandwidth and optimization of multiplexing abilities. When transferring packets of digital data, as used in transmitting Internet information, the noise affects the reading in that detecting each “plus” digit so as to be able to distinguish it from the absence of signal which constitutes a “minus” digit. As noise is reduced, weaker digits can be read more reliably and a lower “error rate” results. For a given “acceptable error rate”, the improvement in signal-to-noise can be used to increase the number of signals that can be multiplexed and maintain the same error rate and thereby increase the system capacity.

[07] The unique techniques used in the processing are intended to be performed digitally to obtain the desired precision and preserve the numerical accuracy. It is consistent with such processing to convert both the signal and the noise introduced at reception to a digital format. Thus, despite the fact that the packets are already digital, it is more consistent to restore the signal to analog and then convert the analog signal plus noise to digital signal-plus-noise. This, in some respects may be unnecessary because the digital packet that is corrupted by the analog receiver noise probably will reduce to the same digital numbers as the more pedantic approach of converting to all analog and then conversion back to digital. These options are discussed later in connection with block diagrams (FIG. 1 and FIG. 3).

[08] The ability to reduce thermal noise limitations that are inherent in any receiving system, beyond the results obtained by averaging several trials, is unique. Such achievement is facilitated by the digital nature of the process (specifically the “storage” of the digitized numbers). However, “being digital” does not in itself produce S/N improvement. Rather, it is the subsequent use of unique iterative processing using an inherent storage and a matrix that serves as a “change sensor” that has unique properties herein described that causes each step to act as a part of a converging iterative procedure with logic that applies to each of the several iterations.

[09] The improvement over merely averaging the noise is the result of improving the “entropy” of the overall process. The processing steps bring a degree of order not initially present. Such order is the result of forcing each and every sample of the noise in a special numerical array to change polarity in a carefully defined manner. This occurs during successive trials, each of which serves as a row of the stored matrix. From this stored information, inherent in the digital matrix, a unique iterative processor, to be described later, will extract the desire information by off-line processing using the signal plus noise flow shown in FIGS. 8 and 9.

SUMMARY OF THE INVENTION

- [10] It is, therefore, an object of the present invention to provide an improved receive system that provides the ability to distinguish between signals with low power levels thereby substantially increasing the S/N ratio of the system.
- [11] A further object of the present invention is to provide a digital processing method, which can be carried out in software and which uses a specially conceived integration of circuit hardware.
- [12] A further object of the present invention is to provide a receive system whereby received signals are sampled periodically and the noise component of the overall received signal sample is estimated from each received signal plus noise sample, leaving nearly noiseless samples of the received signal.
- [13] A further object of the present invention is to provide a method for near-real-time iterative processing, which is performed offline with respect to stored data using a number of iterative steps.
- [14] A further object of the present invention is to provide “polarity change” sensing means, referred to herein as a Topographical Change Sensor (TCS), appropriate for executing the several steps of the noise reducing iterative process.
- [15] A further object of the present invention is to provide an iterative sequence programmer that provides, in “near real-time,” an appropriate control of value steps, and responses to changes sensed by the TCS.
- [16] To achieve the above and other objects of the present invention the following embodiments are provided as examples for the invention. Persons skilled in the art would be aware of techniques available to modify various elements of the invention without straying from the scope and spirit of the invention.

[17] The received signals are amplified and presented to Analog-to-Digital (A/D) converters where digital representations of IN-phase (I) and Quadrature (Q) components of the received signal can be determined. Incidentally, it is in the amplification stage that most of the noise, which comprises the overall system noise, is inherently introduced, as in most receive systems. A phase reference can be established for subsequent processing and the I component, also known as the I-vector can be set to this phase reference while the Q component, or Q-vector can be established 90 degrees out of phase with respect to the reference.

[18] A clock reference for the A/D conversion is synchronized with the phase reference. Each of the I and Q components, thus, contains both signal and noise and may include other unwanted signal as well, in the form of “cross talk”.

[19] Further, a multi-step process is performed on the collected data wherein a matrix for each of the I data, as well as the Q data, samples is initially created which includes several samples of signal-plus-noise data. The data is formatted and applied to an iterative process scheme consisting of an “iterative sequence programmer” and a special topographical number array that serves as a “change sensor”. Upon completion of the iterative processing, “noise-only” data results, which is the net algebraic sum of the different iterative values. The noise-only data is subtracted from the stored signal-plus-noise samples leaving the signal, with the noise greatly reduced.

[20] The invention thereby provides an improvement to the entropy of a sequence of events that have previously occurred and have been stored.

BRIEF DESCRIPTION OF THE DRAWINGS

[21] The objects and feature of the present invention will become more readily apparent from the following detailed descriptions of the preferred embodiments taken in conjunction with the accompanying drawings in which:

- [22] FIGS. 1A, 1B and 1C show typical roles of the invention at different places in the overall information transfer system where the potential improvement is substantial.
- [23] FIG. 2 illustrates an overall signal enhancing system configuration of the system in accordance with a preferred embodiment of the present invention. This block diagram shows an iterative processing scheme including a Topographical Number Array, which is transformed into a Topographical Change Sensor (TCS) in accordance with an embodiment of the invention.
- [24] FIGS. 3A, 3B and 3C illustrate various means of connecting to the signal as provided by the transfer network shown in FIGS. 1A, 1B and 1C.
- [25] FIGS. 4A and 4B are computer printout illustrations of a method of forcing the noise samples to transition through zero at appropriate points in the number array.
- [26] FIGS. 5A and 5B illustrate a method of inputting preprogrammed voltages in “Regular” and “Reverse” manners in accordance with the present invention.
- [27] FIG. 6 is a computer simulation printout illustrating topological groupings of signals plus noise in accordance with the present invention.
- [28] FIG. 7 is a Table of Initial conditions and instructions that help establish the initial or “zero iteration” used in the process described by the Figures that follow.
- [29] FIG. 8 is a logic tree that illustrates how the results from FIG. 7 are utilized to select a next iterative probe value.
- [30] FIG. 9 is a flow diagram that shows how the results from FIG. 8 are used to control the iterative process so as to make it converge and provide a very accurate estimate of the noise for each trial.

[31] FIG 10 illustrates how additional “augmented” logic is coupled to FIG. 8 using information from the history of two or more results from later iterations.

[32] FIG. 11 is a printout indicating the type of numerical results from the above series of operations.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[33] Several aspects of the present invention are specifically directed to improving the signal-to-noise of a received signal beyond what would be predicted by classical information theory. These aspects are important to distinguish. Foremost of these aspects is the “offline” processing of stored information. Storage occurs as analog-to-digital conversion is performed resulting in a series of digital representations of the received data.

[34] Subsequent processing is achieved using modern integrated circuits in an “offline” way that does not adversely affect the bandwidth of the system. Offline processing results in a tolerable transport time delay and as a consequence of the delay, “near real-time” performance results. It is this aspect that presents fundamental opportunities over what can be accomplished in conventional real-time processing. Departure from real-time accounts for a potential to obtain signal-to-noise improvement substantially beyond that achieved by merely averaging noise samples and applying the average to a matched filter. If one were to attempt to attribute the improvement over simple summing, it would lie in the improvement of the entropy of the overall process. The present invention has brought a degree of “order” not previously present.

[35] The order achieved by the present invention is the result of iteratively forcing each and every sample of the noise to change polarity in a carefully defined manner, which is one of the primary aspects of the present invention. The ability to reduce “thermal-noise” affects, inherent in any receiving system, to a level below that which can be achieved by the averaging of several trials is unique. This achievement is facilitated by the “digital” nature of

the process. However, the “digital” nature itself does not produce the achievement. Rather, it is the subsequent use of the digital data in the special iterative process that accomplishes the goal.

- [36] The invention, basically, consists of an improvement to the entropy of a sequence of events that have previously occurred and have been stored.

THEORY OF OPERATION

- [37] The theory of how the “matrix” configuration and its related “iterative process” can determine the “noise value” of each and every trial will be described after a brief introduction about the characteristics of the overall method.

- [38] The conceptual basis of the overall method is an example of the use of numerical logic, (in addition to simple relationship equations) to quantify functions. Such a method is based on examinations in terms of confirmations and contradictions that result from numerical actions. The reduction-to-practice of such a method lends itself to the use of multiple integrated “circuits on a chip” that consists of additions and subtractions. Each step provides a known, yet different numerical impact. The resulting range of consequences is explored by a closed loop iterative processor, which is used to obtain a noise estimate of each sample from each trial. The sensing for such feedback loops requires two inputs name by the average and the input nearest to the average. The sense consequences in near-real time provides the means for overcoming the conventional perceived notion that reduction of noise cannot be extended beyond that which is achieved by simply averaging trials. With the approach, to be described, the noise contribution from each trial can be reduced to values arbitrarily close to zero.

- [39] The block diagram of FIG. 2 will further explain these operations and resulting consequences.

[40] If the input signal (plus noise) is in analog form, an analog-to-digital converter produces a digital representation at an appropriate rate to accommodate signal modulation. The rate is controlled by a local timing clock that samples the incoming signal plus associated “thermal” noise that results primarily from the signal amplification. Each such digital signal or “trial” is injected with i.e. added to, many different predetermined digital values that differ from each other by small increments that are contiguous and encompass virtually the entire minus (m) and plus (n) value range that is probable. These successive and contiguous sums can be considered as (m + n) columns of a matrix with successive trials forming the different rows.

[41] Typically, two successive trials and the average of these trials form a “two by (n+m+1) matrix”. Here “n” represents the number of columns injected with minus values, while “n” is the number injected with plus values and the “1” represents the column of zero injection. Typically n+m+1 might be about 41, or more, to yield a 2x41 matrix. A “third” row might be used under some circumstances, as well.

[42] As a result of the above process the noise component of each signal plus noise sample for each trial will transition from plus-to-minus or minus-to-plus and therefore go “through zero” in a unique column. This is illustrated by FIGS. 4A and 4B. These figures show both the “m” values FIG. 4A and the “n” values FIG. 4B. The first column of FIG. 4B is the “middle/center” column of a combined left and right FIG. 4A. The information is shown separately in a right portion, FIG. 4A, and left portion, FIG. 4B, simply to enhance the readability of the entries. The “zero” or “near-zero” values are circled. Sometimes two adjacent plus and minus columns encompass the zero value and in this case both columns are circled. It must be remembered that FIGS. 4A and 4B are not available in a noise only form and the matrix is constructed by the use of the noise samples in the simulation. They are used here simply to provide an illustration of why the numerical number matrix works. It should

be apparent that when the signal-plus-noise is included, as in the entries shown in FIG. 6, the places where the noise passes through zero will make the entry at these corresponding places, at the “average” and for each of the trials, consist of the signal value, although the identity of such columns are not known until after the iterative process is performed.

[43] The next functional task is to identify for each trial and the average row of the matrix pertinent columns that will become involved in an “iterative” process of several iterations or steps. Each such iteration is a “closed loop process” in which the matrix becomes a “change sensor”. Also as part of this process a series of iterative probes each of known magnitude and “polarity” are used to control the next step of the processing using consequences from the previous step.

[44] The matrix which can be considered a “topological number array” is shown in FIG. 6 and is transformed into a “change sensor” (or detector) by placing the column entries of the two (or more) rows into equilibrium about the zero column which is the topocentric point of the of number array (or matrix). This is accomplished by determining the deviation between the average and the two individual trials. This scalar quantity is independent of the signal and is simply the signal plus noise of each trial minus the signal plus noise average. This can be designated as IA (or QA) where the A indicates that it contains no signal because the signal cancels in the digital plus noise minus the digital plus average noise calculation. IA is simply the deviation value and is known, for the two trial cases the value is plus and minus equal magnitudes. For the “other than average row” this amount of deviation is translated into an equivalent column shift which is simply the magnitude divided by the column spacing to yield an approximate number of column displacements with the polarity of the deviation determining whether the column shift is to the left or to the right.

[45] The preceding measures have partially endowed the topological number array with sensor capabilities; it is necessary in order to complete the task to provide an “error sense” to

its ability to make discrete (i.e. “sharp”) comparisons. Such needed capability is provided by reversing the plus-to-minus formatting of the numbers in one of the rows; the “average” row is selected for this role. Thus instead of preprogramming the contiguous injection number from minus to plus those values are injected in a plus-to-minus fashion, i.e., in reverse order. This provides a bipolar detection or sensing capability.

[46] This is a qualified change detector to accommodate each iterative probe. The necessary response, however, requires the logic provided by the FIG. 8 so that the appropriate iterative steps are used in the process shown in FIG. 9 (and 10) which provide the desired noise estimate for each overall trial when added algebraically.

[47] The iterative process is performed at a slower rate than that for the A/D sampling rate. This is accomplished by using a sub-multiple of the basic clock rate and is shown in FIG. 2 as the “divide by 3”. (This number can be selected as appropriate for each application.) The slower rate requires the storage of the basic samples (the information in the iterative matrix is inherently stored by its nature.) The iterative process can be conducted at a slower rate because the off-line nature of its implementation does not affect the bandwidth handling capability of the system.

[48] Each noise estimate can be cancelled from the signal-plus-noise value of each trial as shown in FIG. 2. This cancellation involves the delay (and storage) of the signal-plus-noise as shown in FIG. 2 so that noise estimates from like trials can be correlated.

DETAILED DESCRIPTION OF OPERATION

[49] Various methods of connecting to the “wirelines” are illustrated by FIGS. 3A, 3B and 3C. The most straightforward to understand is shown in (a) which is simply the use of two wires to obtain two inputs with the “same signal” (with different noise values introduced at each input). This constitutes two trials with like signals and different noise. As such other

amplification and analog to digital conversion results in two signal plus noise column sequences that can be used as two rows of a topological number array or matrix. The process as described previously is applied and will be described in more detail by reference to FIGS. 8 and 9, as follows.

[50] Since this type of connection uses twice as many wire connections, the channel capacity of the network is reduced by a factor of two. Also there is no assurance that the two “signals” will be equal as assumed for illustration. Thus this method is used only to illustrate the “two wire” method; and the other options are resorted to.

[51] The first alternative is to use a single connection and receive two (or more) signal plus noise samples in sequence. This necessitates doubling the sampling rate with a resulting penalty. However, the processing methods remain the same with the two signal-plus-noise samples being used sequentially. This calls for a sampling rate twice as fast as the optimum Nyquist criteria for a modulated signal and results in a sacrifice of 3 dB. However, the tradeoff for near complete cancellation of noise for each successive pair of samples (trials) is significant particularly when considering that the higher noise values are identified and virtually eliminated. This small sacrifice for a potential improvement of 30 dB, or more, net improvement is a worthwhile tradeoff.

[52] A more optimum approach, illustrated in FIG. 3C, involves the use of in-phase and quadrature components as the two signal-plus-noise samples used for forming the topological number array or matrix. This involves sampling the signal (plus-noise) at two phases separated a net of 90 degrees apart; this can be accomplished by a 90°-phase shifter (or an equivalent plus 45 and minus 45 phase shift in the two lines). A more elegant mechanization is to perform this shift as part of the analog-to-digital process by sampling the analog-to-digital converter of the two signals the equivalent to 90 degrees apart. The mechanization is similar but this approach requires processing both the “In phase and quadrature channels

separately. Also the subtraction of the estimated signal from the actual signal is a vector subtraction that takes place at a 45 degree reference.

[53] As mentioned previously as part of the Theory of Operation the first numerical action consists of deliberately changing the polarity of the noise portion of the signal-plus-noise voltage of each and every trial. This forced change is accomplished by providing a series of contiguous voltage increments that span a range of about minus one volt (-1.0) to plus one volt (+1.0) in increments on the order of .05 volts or less, thereby forcing the change of polarity to occur in one column, or an adjacent pair of columns. There is much flexibility in the number of voltage increments and the “graduation” of their spacing. As each “trial” of signal plus noise is entered into this series of columns, a network of trials, or matrix, is formed which is referred to as a topographical number array or matrix. The addition of the value inserted into each column creates a “new noise” for each column. As a consequence of these additions, there results one column in which the net polarity of the noise-only portion changes (because the voltage in that column is opposite to that of the noise). While this is focused usually in a specific column, this condition can occur between adjacent columns giving rise to interpolation between adjacent columns.

[54] A series of trials, each consisting of independent samples of signal-to-noise was used in simulation. It corresponds to each digital value obtained from the A/D converter each time it is sampled by the A/D clock. This rate is appropriately chosen to accommodate the modulation and its characteristics (particularly “bandwidth”) of the information to be received.

[55] The “noise model” used for the simulation of the “stream” of signal-to-noise trials is based on generally accepted Gaussian type of probability density distributions as used on radar and communications receiving systems. While such distribution is significant in analytical treatments, the non-real time methods used here are less susceptible to the noise

model. This is because each unwanted noise sample is literally forced to a value that goes through zero at each point of inflexion. This is controlled by its relative positioning in the “topographic array”.

[56] As a result of the above steps, each trial has a unique column whose relative location in the network corresponds to the magnitude of the noise portion of that trial and whose + or – polarity location is opposite that of the actual noise polarity. Thus, the “noise entry” in the matrix will transition through zero in such a correlated column.

[57] To illustrate this, FIGS. 4A and 4B was constructed using the simulated values of the noise. (These are not available in practice but serve here merely to illustrate the nature and effectiveness of the array of columns to accurately “locate” the noise within the topographical matrix.)

[58] To extract the information (as to column choice) it is important that part of the information is introduced or “scanned” into the iterative matrix in a special way that will produce a “proper error sense” when making voltage comparison between entries from two appropriate columns. The pertinent columns are either of the first or second trials and the average of these two trials. To accomplish this, the “average row” is scanned (i.e., the voltages introduced) in a minus to plus manner, which is the opposite of that used in trial 1 and trial 2, as shown in FIG. 5A. This insures that the error sense at the different “zero noise transition points” are opposite rather than “parallel”, i.e., having like slopes as illustrated in FIG. 5B. The appropriate “zero noise comparisons” are made between each trial and the average. Such comparisons can locate the column location to a high degree of sharpness or precession using interpolations of the values from adjacent columns.

[59] The deviations from the average are used because of two or more trials, i.e., samples of signal plus noise are averaged, then the algebraic sum of the two or more deviations from

the average is zero. This concept is one of the foundations of making the process independent of signal level, i.e., the deviation from the average contains no signal and is expressed as 1A or QA. (The “deviation from Average” is simply equal to the actual I (or Q) minus the average I (or Q) of the two (or more) of trials.) Since both I and I average contain the signal, the subtraction of the average I from the actual I cancels the signal.

[60] The topological number array is rendered a change detector by applying a column shift process in which this shift is equal in amount to the magnitude of the deviation and the shift direction, i.e., left or right, is determined by the polarity of the deviation. The amount of shift is the deviation amplitude divided by the column spacing increments to express the result in number of columns. This amount of shifts is made in the “deviation from average row” and places the two rows (the deviation from average row and the average row) in equilibrium about the zero column, which is the topographic column. An equilibrium condition, such as shown in FIG. 6, qualifies the matrix as a change detector because any further disturbances in either row introduced by an iterative probe will create a mismatch of column readings and lead to a new location for the match of the column entries; such a new match will occur in either the left or right portion of the topological array.

[61] A flow diagram is shown by FIG. 8 and FIG. 9 consisting of the logical selection for each iterative probe, FIG. 8, and the control of the iterative steps, FIG. 9.

[62] Starting with the condition given in FIG. 7 which contains the instructions for obtaining the initial column match, the consequences of such a match are determined by the decision logic in FIG. 8. The first determination is whether the “new” match (caused by the initial iterative probe) occurs in the left or in the right portion of the topographical number array sensor. The next question resolved is whether or not this situation represents a change in side with respect to its initial location (i.e. from left to right or vice versa). A “yes” in either indicates that a change in polarity has occurred indicating the use of the new polarity

for the next iteration. In recognition that the first iteration was sufficient to cause such a significant change, the next probe should be of a smaller amount and the diagram shows the former value is halved to cause the match from the next probe to occur closer to zero.

[63] The absence of a change of polarity (i.e. same side location) indicates that a probe of the same polarity as the previous one should be continued for the next probe. However, it is now desired to know whether the prior probe caused the resulting column match to occur closer to, or farther from zero. The closer the match is to zero is an indication that the process is converging and a smaller value (such as one half) should be a good choice unless it causes “overshoot” (i.e. change of polarity on the next iterative probe). However, if this probe is not halved and a large amount of noise is present, the process should converge faster, until the polarity changes; if it is found that more than 2 iterations result in continued maintenance of the same polarity; if this occurred the 3rd and or 4th iteration will benefit by not continuing to halve of the magnitude of the next probe. This will occur until a polarity change is noted. Such downstream iterations are shown in FIG. 10 which is referred to as “augmented logic” reflecting iteration history.

[64] If the consequences of the process makes the match farther from zero, a divergence is indicated which calls for the next probe value to be of opposite polarity and greater than 1 to make up for the loss of convergence during this probe. A nominal value such as 1.5X for the next iteration is shown on the diagram of FIG. 8.

[65] Using such logic, the choice of one of six different options available is indicated in the form of an appropriate magnitude and polarity for the next probe.

[66] It should be perceived that the selections of a series of appropriate probes as illustrated in FIGS. 8 through FIG. 10 are representative of the process that determines the “speed” of convergence to use a smaller number of iterations to cope with the larger value of

noise the magnitude selections process might eliminate the $\frac{1}{2}$ where a question mark is shown.

[67] FIG. 12 shows a computer printout of typical results of the process illustrated by FIGS. 8 and 9 using appropriate iterative programming to achieve the results shown. These represent the results for each trial of converging results show by the middle (and next to last column), of the noise values after each of six iterative steps. The “original” average noise for each trial is shown in the first column after the trial number, for 36 independent trials (3 per entry number) for I’s, (not shown) and Q’s, (FIG. 12). (The I and Q results have been processed independently).

[68] The key results are shown in the “Equivalent voltage added” column which is the algebraic sum of all the plus and minus voltage probes all of the iterations and represent the “output” as shown previously in FIG. 9. This column provides the sought-after estimate of the magnitude of the noise, note that these values closely approximate the magnitude of the actual noise show in the first column. The polarity of this magnitude will be opposite as shown clearly in the printout. The ratios of these estimated magnitude values to the original are shown in the last column. These vary widely although the estimate values as very close to the actual as expressed by the residual noise in the nest to the last column. The ratio column is in terms of voltage comparison so that a ratio of 10 corresponds to a “20 dB” improvement.

[69] The noise residuals are generally within $\pm .02$ volts. This value is determined for the most part by the number of iteration and will diminish to a value ultimately determined by the accuracy of the digital measuring ability of the process. Which will depend on how well the embodiment is executed.

[70] The ratio must be interpreted carefully because frequently the lower ratio corrupted to noise that is already low relative to the “norm”. Taking this into account perusal of the result indication an integrated improvement of about 30 dB; with one or two more iteration this improvement would improve by 6 or 8 dB.

USE OF INTEGRATED CIRCUITS

[71] The operations previously described can be implemented by means of integrated circuit “chips”; such a semi-conductor integrated circuit first provides the means for injecting a series of predetermined digital values furnished by, for example, preprogrammed Electrically Programmed Read Only Memory (EPROM) that also memorize the resulting “new” digital values. The result is a stored topographical number matrix of two rows which can serve as a means of sensing changes in each step of an iterative process or 4 or more steps. Each step includes an iterative probe, which cause a column displacement that is determined by its magnitude and polarity. The information in two appropriate columns is compared by a digital number compared to obtain a match when these entries are approximately equal. This is in accordance with FIG. 2.

[72] As a result of the location of this match the next iterative value prove is determined by using the logic shown in FIG. 8. Such a new probe value consists of its magnitudes and polarity. This process is repeated for each of the several iterations and the algebraic summation of the probe values the “chips: with an accurate estimate of the noise portion in the form of a magnitude and a polarity (the latter is opposite to that of the noise.) This permits a nearly complete noise cancellation from the stored (and delayed original signal plus noise sample for each trial. As a result of the iterative converging process on the chip each trial provides a “frame of enhanced signal information.” A series of such trials or samples accommodate the modulations of the signal to provide a stream of modulated signals.

[73] It should be noted that in the above process key information from the decision tree of FIG. 8 is given as to whether, after each iterative probe the column match occurs either closer to or further from the center column.

[74] Each iteration starts with the conditions from the end of the previous one and each series provides a frame of information to accommodate signal modulation. Several chips (or subsection of one chip) can be used in a form of parallel processing of different complete iterative frames. One or more such frame can be separately and simultaneously formed and stored to increase the speed of processing of each frame.

[75] For each entry, the column choice is employed to carry out the process to obtain the estimate for each trial. The value readout can be determined more accurately by interpolation between adjacent column. This process can provide a precision of determination of about .01 volts as determined digitally by the column readings described next. A voltage of such precision is subtracted algebraically from each noise sample in an ongoing manner to provide a continuous stream of information from which the noise has been dramatically reduced.

[76] The “Column readers” shown in FIG. 2 provide two different types of information. The first is used to help locate the relative positions of the columns that contain the pertinent information. This requires a “match” of the readings between two entries in these two columns. The second is the value of the column value calibration as determined by its location.

[77] The ability to sense a smaller value can be enhanced if the signal plus noise “voltage” range is increased, by amplification to ± 3 or ± 5 volts. This is in terms of digital values. To this end, the same “ ± 1 volt chip” can be time shared 3 or 5 times to provide segmented coverage of the total range of appropriate sequential bias values for each replication. This approach of course, when performed “offline”, will increase the time delay imposed by the

processing and thereby influence the “speed” requirement of the processing chips. Storage capacity is provided to accomplish the expanded iterative network (or matrix) coverage so as to permit the access to be performed.

INTERNET SYSTEM CONFIGURATION

[78] FIG. 1A shows a simplified but comprehensive representation of the overall scheme used to provide a communication system suitable for the Internet. The key elements shown in addition to “the sender” and the “receivers” (by stations or by individual users) are 1) the “Service Provider” facilities, 2) an overall network for “Switching and Routing,” and 3) Localized Distribution means.

[79] The key functioning elements in this overall network are the “gateways” at the entrance of the Service Provider and the gateways of the Localized Distribution System to which the message (coded as to distribution) is routed; the gateway of the ultimate user is the remaining link of the process. Shown are the means of using analog information at the gateways and eventually using the received information at the user also in analog form. This is the result of using messages historically at audio frequencies (sometimes involving a “carrier” as well).

[80] These capabilities will need to go beyond historical practice when the information is expanded to include data used for business and special function beyond “voice” and with an even greater demand when video frequencies are accommodated.

[81] FIG. 1B illustrates an embodiment of the invention at a gateway receiving the signal in analog forms (at audio or video frequencies). Sometimes a carrier frequency is used, which is modulated in various ways by such frequencies. FIG. 1B represents the invention in its more basic form.

[82] FIG. 1C illustrates an embodiment for which a auxiliary step is required which can be provided, for example, by a modem, when receiving a signal in digital form and which can occur in digital packets of information which have been “compressed” as part of the bandwidth accommodation characteristics of the network. In such an instance a decompression means is used before a digital to analog conversion.

[83] FIG. 1B is referred to for simplicity of explanation, as an “A/D gateway” while FIG. 1C is called a “D/A gateway”.

[84] The roles and function of the invention can be explained in such use by reference to the overall system scheme illustrated as FIG. 1C; accordingly.

[85] The Service Provider receivers the many separate signals from many senders with such signal being accommodated by gateways which in the instance should employ the embodiment of the invention shown in FIG. 1B to provide an enhanced signal in digital form as shown in the diagram. This implementation is a simplified block diagram of the more comprehensive block diagram of FIG. 2 that has been explained previously.

[86] The total process including the iterative probing, sensing and decision making using feedback method for several iterations or steps and these are referred to as the one-block “Iterative Processor”. Such a processor provides an accurate noise estimate for each trial and this is “subtracted” from each signal-plus-noise sample to remove most of the actual noise; this step requires an appropriate (fixed) time delay to correlate or align, corresponding samples. As explained throughout this specification the processing occurs in “near real-time” to provide the opportunity for the signal-to-noise enhancement. Such enhancement takes place between points A & B so that an enhanced signal is furnished to the “Multiplexing” and Assignment shown within the Service Provider block. The multiplexing permits more than one “message” to be carried on a single line, which can be a wire or fiber optic strand.

[87] At point B this enhancement of relative signal strengths could be 30 dB or more relative to the noise that would be present without this embodiment and such an improvement can be used, in part to provide more effective multiplexing to achieve greater channel density. An alternative to using all, or part of the “30 dB” improvement would be the ability to accept weaker signals so that signals from a farther distance away could be accommodated. A typical allocation might be 10 dB for longer “range” and 20 dB for more channel capacity for the same basis of the Service Provided signal to the switching and routing network. Such a signal might be provided in “packets” of digital information that have undergone compression of modulated information. This process occurs after the enhancement and the primary role of FIG. 1B is to provide a stronger signal.

[88] The switching and routing network has a wide range of technology using wire and fiber optics in accordance with an “internet protocol” that sends the signals to the Localized Distribution System. The gateway at the Localized Distribution System would use the embodiment of the invention as augmented by the addition as shown in FIG. 1C; here the signal received as a result of routing consisting of compressed packets that require decompression before the digital to analog conversion. Because of the enhancement potential of the invention a smaller signal can be accepted here, relative to noise. For a given acceptable digital error rate a strong analog signal is delivered to the Multiplexing portion of the Distribution system. This improvement can enable such multiplexing to accommodate more channels (i.e. increased channel capacity) here as well as in previous multiplexing.

[89] Such a signal is delivered to the final gateway at the receiver of the user; here the version of FIG. 1B is used again. The enhancement now permits a weaker receive signal (relative to noise) to be received. Those enhancements can be used to extend the range to the user as well as help accommodate the addition channel provided at the distribution system by the enhancement of gateway #2 (FIG. 1C). The enhanced signal resulting from gateway 3

can be used to provide a stronger signal that will facilitate realization of a shorter time to acquire (i.e. access) the Internet connection and help the “last mile” of the reception link.